TECHNICAL SPECIFICATIONS

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Oyster

Design
2 in - 6 out Digital Signal
Processing Controller

Inputs
Two analogue (electrically balanced)

Outputs
Six analogue (impedance balanced)

Input Impedance
10 kΩ unbalanced / 18 kΩ balanced

CMRR
>50 dB 30 Hz – 20 kHz

Output Impedance
50 Ω unbalanced / 100 Ω balanced

Max input voltage
24 dBu peak (12.3 Volts)

Max output voltage
12 dBu peak (3.1 Volts)

Delay step
1 µs

Adjustable delay
Up to 1 second per each input and
output channel (up to 2 seconds total)

Channel gain
-48 to +12 dB per input and
output channel

Routing matrix gain
-48 to +12 dB

Parametric EQ filters
Up 10 per each input and output
channel

Parametric EQ filter types
Bell,
high/low shelf,
high/low pass,
notch,
all pass

Crossover Filter - Slopes
6, 12, 18, 24 dB per octave

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Crossover Filter - Types
Bessel,
Butterworth or Linkwitz-riley

Limiter threshold
-48 to +12 dBu peak

Mains
85 - 265 V AC, 50-60Hz~

Consumption
<10 watts

Zero-attack peak limiters
1 per output channel
(end of the signal chain)

Connectors
Zero-attack peak limiter
with threshold adjustment
in 0.25 dB steps

Fuse
T500 mA L250 V